

PRODUCT SPECIFICATION

Part No.:	AC-QF-3G40-02	
Description:	40G QSFP+ Transceiver, PSM4 1310nm 2km	
Release Date	Rev.	Revision Change Description
2017/06/07	A0	New Release
2020/12/28	A1	Template Update

Features

- ✧ Transmission data rate up to 11.2Gbps per channel
- ✧ Digital Diagnostics Monitoring Interface
- ✧ Hot-pluggable QSFP+ form factor
- ✧ Full duplex 4 channel 1310nm parallel module
- ✧ Commercial operating case temperature range: 0~70°C
- ✧ RoHS-6 Compliant
- ✧ Maximum power consumption 2.5W
- ✧ Single +3.3V power supply
- ✧ Maximum link length 2km over SMF
- ✧ MTP/MPO optical connector

Application

- ✧ 10/40GBASE-LR/W 10/40G Ethernet
- ✧ Infiniband QDR, DDR and SDR
- ✧ Data Centers and Storage Arrays

Standard

- ✧ Compliant to IEEE 802.3ba
- ✧ Compliant with QSFP+ MSA
- ✧ Compliant to SFF-8436

Specification:

Absolute Maximum Ratings				
Parameter	Symbol	Min	Max	Unit
Storage Ambient Temperature	T _{STG}	-40	85	°C
Operating Humidity	H _O	5	95	%
Power Supply Voltage	V _{CC}	0	3.6	V
Damage Threshold, each Lane	THd		3.3	dBm

Recommended Operating Conditions					
Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature (Commercial)	T _c	0		70	°C
Power Supply Voltage	V _{CC}	3.13	3.3	3.47	V
Power Consumption	P _{diss}			2.5	W
Signaling Speed Per Channel			10.3125	11.2	Gbps
Control Input Voltage High		2		V _{CC}	V
Control Input Voltage Low		0		0.8	V
Fiber Length 9/125μm core SMF		-	-	2	KM

Electrical transmitter Characteristics						
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Transceiver Power-on Initialization Time				2000	ms	1
Single-ended Input Voltage Tolerance (Note 2)		-0.3		4.0	V	Referred to TP1 signal common
AC Common Mode Input Voltage Tolerance			15		mV	RMS
Differential Input Voltage Swing Threshold		50			mVpp	LOSA Threshold
Differential Input Voltage Swing	V _{in,pp}	190		700	mVpp	
Differential Input Impedance	Z _{in}	90	100	110	Ohm	
Supply Current	I _{CC}			1.1	A	
Maximum Power Consumption				3.5	W	
Maximum Power Consumption – LP Mode				1.5	W	
J2 Jitter Tolerance	Jt2	0.17			UI	
J9 Jitter Tolerance	Jt9	0.29			UI	
Data Dependent Pulse Width Shrinkage (DDPWS) Tolerance		0.07			UI	
Eye Mask Coordinates {X1, X2, Y1, Y2}			0.11, 0.31 95, 350		UI mV	Hit Ratio =5x10 ⁻⁵

Signaling Speed Operating Range			± 100		ppm	
Transmitter Differential Input Impedance			100		ohms	
Transmitter Differential Input Voltage		0.2		1.6	V	
Electrical receiver Characteristics						
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Single-ended Output Voltage		-0.3		4.0	V	Referred to signal common
AC Common Mode Output Voltage			7.5		V	RMS
Differential Output impedance			100		mVpp	
Differential output voltage		300		850	mV	
Termination Mismatch at 1MHz				5	%	
Differential Output Return Loss		See IEEE 802.3ba 86A.4.2.1			dB	10MHz - 11.1GHz
Common Mode Output Return Loss		See IEEE 802.3ba 86A.4.2.2			dB	10MHz - 11.1GHz
J2 Jitter Output	Jo2			0.42	UI	
J9 Jitter Output	Jo9			0.65	UI	
Eye Mask Coordinates {X1, X2 Y1, Y2}		0.29, 0.5 150, 425			UI mV	Hit Ratio = 5×10^{-5}

Optical transmitter Characteristics						
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Data Rate Per Lane			10.3125	11.2	Gbps	
Relative Intensity Noise (RIN)				-128	dB/Hz	
Average Launch Power per lane @ TX off state				-30	dBm	
Transmitter Reflectance				-12	dB	
Optical return loss tolerance				12	dB	
Transmitter Eye Mask definition: X1,X2, X3, Y1, Y2, Y3		Compliant with 802.3ba standard {0.25, 0.4, 0.45, 0.25, 0.28, 0.4}				
Average Launch Power (each lane)	P out	-8.2		2	dB	
Optical Modulation Amplitude (each lane)	OMA	-5.2		3	dBm	
Lane center wavelengths (range)	λ	1290	1310	1330	nm	
Side Mode Suppression Ratio	SMSR	30			dB	
Extinction Ratio	ER	3.5			dB	
Transmitter Eye Mask Definition {X1, X2, X3, Y1, Y2, Y3}		{0.25, 0.4, 0.45, 0.25, 0.28, 0.4}				
Optical receiver Characteristics						

Parameter		Symbol	Min	Typical	Max	Unit	Notes
Receiver sensitivity in OMA, each lane					-12.6	dBm	
Average Power at Receiver			-13.7		2.3		
Stressed Receiver Sensitivity in OMA					-10.3	dBm	
LOS	Optical De-assert	Pd			-14	dBm	
	Optical Assert	Pa	-30				
LOS hysteresis			0.5			dB	
Receiver Electrical 3 dB upper Cutoff Frequency, each Lane		Fc			12.3	GHz	

Notes:

- The maximum transmitter average optical power of 1.5 dBm is well within the guardband of receiver overload specifications of commercially available 10GBASE-LR SFP+ transceivers offered by LONTE.

Pin Definition

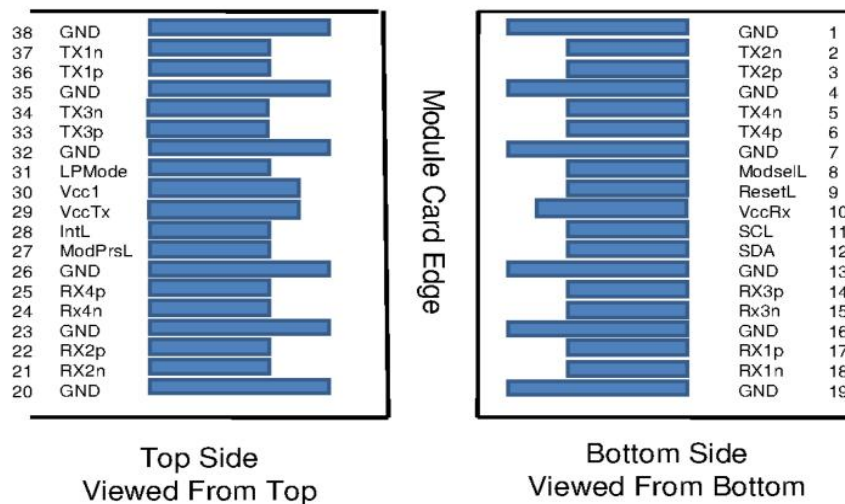


Figure1 QSFP MSA-compliant 38-pin connector

Pin	Symbol	Name/Description	Notes
1	GND	Transmitter Ground (Common with Receiver Ground)	1
2	TX2N	Transmitter Inverted Data Input	
3	TX2P	Transmitter Non-Inverted Data Input	
4	GND	Ground	1
5	TX4N	Transmitter Inverted Data Input	
6	TX4P	Transmitter Non-Inverted Data Input	
7	GND	Ground	1
8	ModSelL	Module Select	
9	ResetL	Module Reset	

10	Vcc Rx	+3.3 V Power supply receiver	2
11	SCL	2-wire serial interface clock	
12	SDA	2-wire serial interface data	
13	GND	Ground	
14	RX3P	Transmitter Inverted Data Input	
15	RX3N	Transmitter Non-Inverted Data Input	
16	GND	Ground	1
17	RX1P	Transmitter Inverted Data Input	
18	RX1N	Transmitter Non-Inverted Data Input	
19	GND	Ground	1
20	GND	Ground	1
21	RX2N	Transmitter Inverted Data Input	
22	RX2P	Transmitter Non-Inverted Data Input	
23	GND	Ground	1
24	RX4N	Transmitter Inverted Data Input	1
25	RX4P	Transmitter Non-Inverted Data Input	
26	GND	Ground	1
27	ModPrsL	Module Present	
28	IntL	Interrupt	
29	Vcc Tx	+3.3 V Power supply transmitter	2
30	Vcc1	+3.3 V Power Supply	2
31	LPMode	Low Power Mode	
32	GND	Ground	1
33	TX3P	Transmitter Inverted Data Input	
34	TX3N	Transmitter Non-Inverted Data Input	
35	GND	Ground	1
36	TX1P	Transmitter Inverted Data Input	
37	TX1N	Transmitter Non-Inverted Data Input	
38	GND	Ground	1

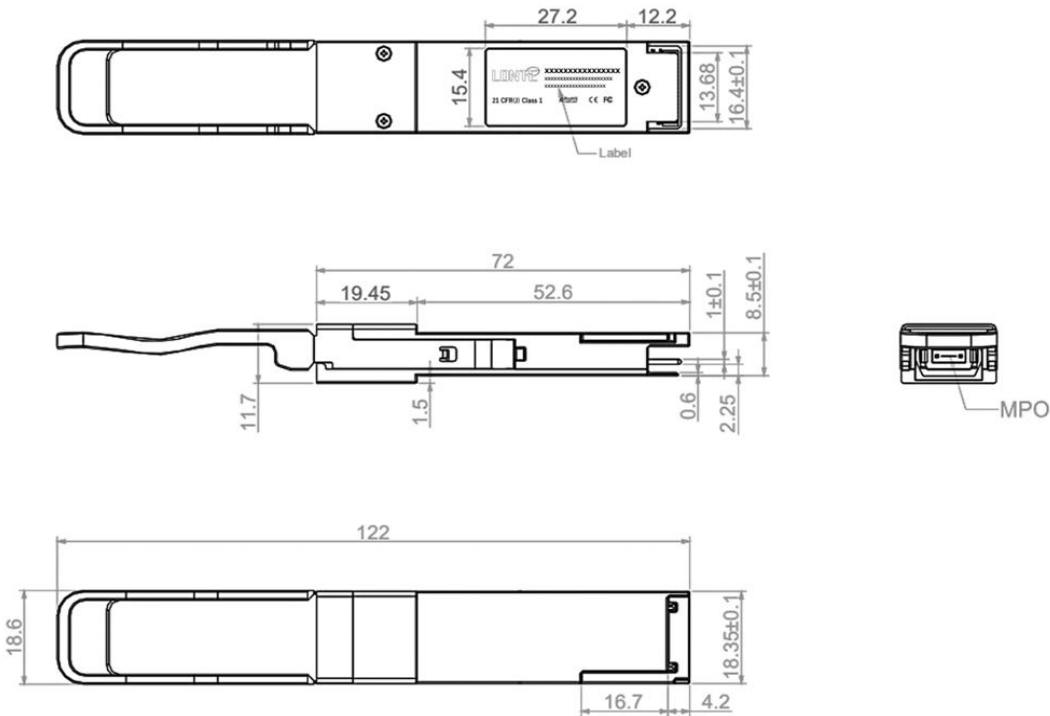
Table 1: QSFP Module PIN Definition

Notes:

1. GND is the symbol for signal and supply (power) common for QSFP+ modules. All are common within the QSFP+ module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal common ground plane.
2. VccRx, Vcc1 and VccTx are the receiving and transmission power suppliers and shall be applied concurrently. Recommended host board power supply filtering is shown in Figure 3 below. Vcc Rx, Vcc1 and Vcc Tx may be internally connected within the QSFP+ transceiver module in any combination. The connector pins are each rated for a maximum current of 500mA.

Package Outline

Dimensions are in millimeters. All dimensions are $\pm 0.2\text{mm}$ unless otherwise specified. (Unit: mm)



Regulatory Compliance

Feature	Test	Method
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883E Method 3015.7	Class 1(>1000V for SFI pins, >2000Vfor other pins.)
Electrostatic Discharge (ESD) Immunity	IEC61000-4-2	Class 2(>4.0kV)
Electromagnetic Interference (EMI)	CISPR22 ITE Class B FCC Class B CENELEC EN55022 VCCI Class 1	Comply with standard
Immunity	IEC61000-4-3	Comply with standard
Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN (IEC) 60825-1,2	Compatible with Class I laser Product

Ordering information

Part. No	Specifications								
	Pack	Rate (Gbps)	Tx (nm)	Po (dBm)	RX	Sen (dBm)	Temp (°C)	Reach (km)	DDM
AC-QF-3G40-02	QSFP+	41.25G	DFB 1310	-8.2~2	PIN	<-12.6	0~70	2	Y